

A universal method for calculating and extracting the LF and RF noise behavior of nonlinear devices

R. Follmann, J. Berben, D. Köther, P. Waldow, J. Borkes and I. Wolff
 Institut für Mobil- und Satellitenfunktechnik IMST GmbH,
 D-47475 Kamp-Lintfort, Germany, e-mail follmann@imst.de

Abstract—In this paper an efficient method for calculating the noise behavior of active devices is described. This method offers a general solution for a large variety of transistor and other devices. The method can easily be implemented in any extraction and simulation software and allows determination of LF as well as RF noise behavior. The functionality is demonstrated on a nonlinear FET model [1], [2]. Furthermore, verifications with HP series IV/ADS [3] are given for different devices.

I. INTRODUCTION

The design of many circuits requires the extraction and the simulation of noise parameters for quite different noisy elements like FETs, BJTs or HBTs. In the past, several papers have been presented describing an extraction process for different noise models [4], [5]. Many of these methods are limited to special kinds of active devices. Most CAD circuit simulation software does not allow the extraction of parameters, although it has implemented functions to calculate noise. For noise extraction exactly these functions must be known in order to fit e.g. noise function coefficients to measurements during an optimization process. Therefore, in this paper we present an efficient method for calculating noise behavior of any device. Our proposed method is based upon the Y-matrix of any equivalent circuit and a matrix containing the noise sources for that equivalent circuit. With that knowledge both LF as well as RF noise behavior can be extracted and calculated. We describe how our proposed method can be used for extracting and simulating noisy circuits. The method can be applied to any extraction and simulation software.

II. TRANSFORMING NOISE SOURCES

When simulating a circuit, noisy n-ports must be transformed into a non-noisy n-port and associated noise sources at outer ports. Fig. 1 shows an example of transforming noisy networks to non-noisy ones including a

current source and a voltage source at the output for the 2-port. Using the transformation matrix

$$[T]^{(y \rightarrow a)} = \begin{bmatrix} 0 & -\frac{1}{y_{21}} \\ 1 & -\frac{y_{11}}{y_{21}} \end{bmatrix}, \quad (1)$$

the noise matrix of currents can be calculated in a form

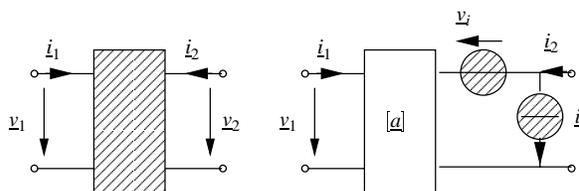


Fig. 1. Transforming noise sources to outer ports.

using equation (2):

$$\begin{bmatrix} v_i^{(a)} \\ i_o^{(a)} \end{bmatrix} = [T]^{(y \rightarrow a)} \begin{bmatrix} i_i^{(y)} \\ i_o^{(y)} \end{bmatrix}, \quad (2)$$

whereby the indices i and o describe input and output ports. For calculating noise power, the column vectors are multiplied with the conjugated row vectors of the noise source matrix and end up by division with $4kT\Delta f$ in the wellknown correlation matrix

$$\begin{aligned} [C]^{(a)} &= \frac{1}{4kT\Delta f} \left(\begin{bmatrix} v_i \\ i_i \end{bmatrix} \begin{bmatrix} v_i^* & i_i^* \end{bmatrix} \right) \\ &= \frac{1}{4kT\Delta f} \begin{bmatrix} v_i v_i^* & v_i i_i^* \\ i_i v_i^* & i_i i_i^* \end{bmatrix}. \end{aligned} \quad (3)$$

III. NOISE ANALYSIS

There are different ways of calculating noise. One way is the division of a circuit into different sub-2-ports with noise sources, that have been transformed as described above. Noise behavior for each 2-port can be easily calculated e.g. using node analysis. The separately calculated 2-ports can be combined using the transformation matrices.

In this paper a different approach is used. Taking into account the Tellegen theorem, current and voltage sources can be transformed to the outer ports of a network N . To do this, the adjuncted network \hat{N} is required, which has a definite reversible relation to the network N . The Y-matrix of the adjuncted network \hat{N} represents the transposed Y-matrix of the network N as well. With that knowledge, a current source e.g. can be transformed to the outer ports using just one node analysis for the adjuncted network and $k - 1$ analysis for the network N ,

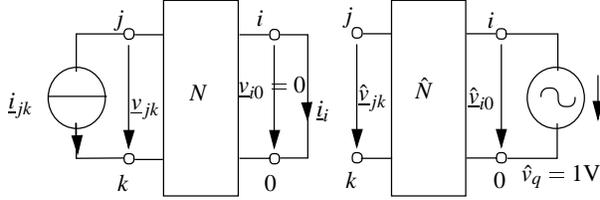


Fig. 2. Circuitry of networks for using the Tellegen theorem.

whereby k gives the number of nodes in the network N . Using a circuitry as shown in fig. 2, the current transformation function α_i is equal to the negative voltage transformation function of the adjuncted network \hat{N} :

$$\alpha_{i,jk} = \frac{i_i}{i_{jk}} = -\frac{\hat{v}_{jk}}{\hat{v}_q}. \quad (4)$$

This fact can be used for calculating noise behavior as shown for an example in the next section.

IV. CALCULATING FET NOISE

The applicability of the Tellegen theorem is shown for a FET equivalent circuit (fig. 3). The circuitry takes care of different noise mechanisms in FETs, like resistive noise and channel noise. Furthermore, $1/f$ noise is taken into account. The ten nodes of the equivalent circuit are num-

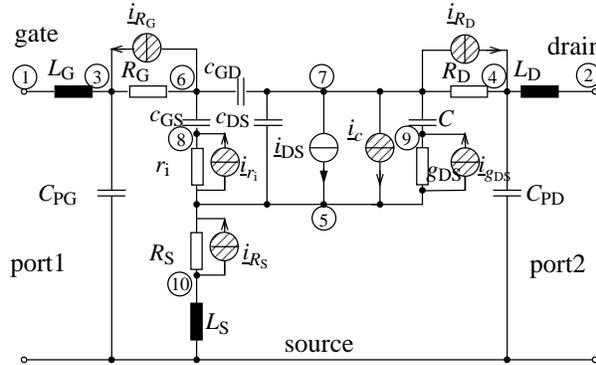


Fig. 3. Noisy equivalent circuit for a FET.

bered in order to determine the 10×10 Y-matrix. In correspondence with the Y-matrix, a 10×10 noise matrix can be defined. The position of noise sources in that matrix is given by the node numbers at the end of the noise source. The direction of the current is defined in the way, that at matrix entry $i_{j,k}$ the noise current in the equivalent circuits flows from node j into the direction of node k . All noise current sources have only positive entries. The factor $\sqrt{4kT\Delta f}$ is placed outside the brackets. For the presented equivalent circuit the noise current matrix is given to

$$[i_N] = \sqrt{4kT\Delta f} \cdot \begin{pmatrix} 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & i_{5,8} & i_{5,9} & 0 \\ 0 & 0 & i_{6,3} & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & i_{7,4} & i_{7,5} & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & i_{10,5} & 0 & 0 & 0 & 0 & 0 & 0 \end{pmatrix} \quad (5)$$

with the resistive noise

$$i_{5,8} = \sqrt{\frac{1}{r_i}},$$

$$i_{5,9} = \sqrt{g_{DS}},$$

$$i_{6,3} = \sqrt{\frac{1}{R_G}},$$

$$i_{7,4} = \sqrt{\frac{1}{R_D}},$$

$$i_{7,5} = \sqrt{g_{ic}},$$

$$i_{10,5} = \sqrt{\frac{1}{R_S}}$$

including the channel noise

$$g_{ic} = \frac{T_{sim}}{T_0} \frac{2}{3} g_m + k_f \frac{I_c^{\alpha f}}{f^{b} 4kT_0}. \quad (6)$$

The next calculation steps are converting the Y-matrix into an Z-matrix, and re-calculating the 2×2 Y-matrix because of the examined 2-port and computing the transposed Y-matrix

$$[y_T] = [y]^T. \quad (7)$$

The adjuncted network shown in fig. 4 is used in order to calculate a voltage transformation function. The volt-

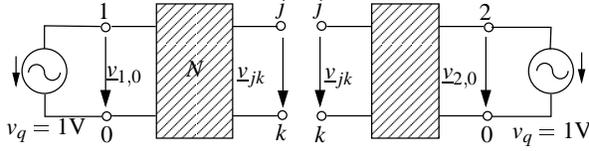


Fig. 4. Using the Tellegen theorem for noisy circuits.

age source v_q can be transformed into a current source as shown in equation (8)

$$\begin{aligned} i_{Q_1} &= \frac{v_q}{\underline{y}_{T,2 \times 2_{1,1}}}, \\ i_{Q_2} &= \frac{v_q}{\underline{y}_{T,2 \times 2_{2,2}}}. \end{aligned} \quad (8)$$

Using the Gauß algorithm, the equation system containing the transposed Y-matrix $[\underline{y}_T]$ can be solved for the voltages:

$$\begin{bmatrix} i_{Q_1} \\ \vdots \\ i_{Q_{10}} \end{bmatrix} = \begin{bmatrix} y_{T_{1,1}} & \cdots & y_{T_{1,10}} \\ \vdots & \ddots & \vdots \\ y_{T_{10,1}} & \cdots & y_{T_{10,10}} \end{bmatrix} \begin{bmatrix} v_1 \\ \vdots \\ v_{10} \end{bmatrix}. \quad (9)$$

The result delivers a voltage transformation factor, that is equal to the negative current transformation factor. Now, for each noise source the influence at the input and output port of the example 2-port can be calculated. Taking care of the correlation of the noise sources, the influences of the single noise sources can be added to one single noise source at the input as well as the output. At this point a noise free 2-port with two outer noise current sources in Y representation has been determined. With the two noise sources, the correlation matrix can be calculated as described in equation (11). With the help of the transformation matrix $[T]^{(y \rightarrow a)}$, the correlation matrix can be changed into the following representation:

$$[T]^{(y \rightarrow a)} = \begin{bmatrix} 0 & -\frac{1}{\underline{y}_{2 \times 2_{21}}} \\ 1 & -\frac{\underline{y}_{2 \times 2_{11}}}{\underline{y}_{2 \times 2_{21}}} \end{bmatrix}, \quad (10)$$

$$[\underline{C}]^{(a)} = [T]^{(y \rightarrow a)} [\underline{C}]^{(y)} [T]^{(y \rightarrow a)+}. \quad (11)$$

Now, the noise figure can be calculated to

$$F = 1 + \frac{T_{sim} |\underline{y}_G|^2 C_{11}^{(a)} + C_{22}^{(a)} + 2\Re\{\underline{y}_G C_{12}^{(a)}\}}{g_G}, \quad (12)$$

whereby T_{sim} is the simulation temperature and \underline{y}_g the generator admittance. The noise resistor is equal to

$$R_n = \frac{T_{sim}}{T_0} C_{11}^{(a)} \quad (13)$$

and the optimum reflection coefficient is

$$\Gamma_{G_{opt}} = \frac{1 - \underline{y}_{G_{opt}} Z_0}{1 + \underline{y}_{G_{opt}} Z_0}. \quad (14)$$

The $1/f$ noise can be calculated using the following equation:

$$V_{NF}[\text{dBm}/\sqrt{\text{Hz}}] = 20 \log \left(\sqrt{\frac{T_{sim}}{T_0}} \cdot i_{o1} i_o^* \frac{50 \Omega}{50 \Omega \Re(\underline{y}_{2 \times 2_{22}}) + 1} \cdot 1000 \right). \quad (15)$$

V. VERIFICATIONS

In this section some verifications of the proposed method for calculating and extracting noise are presented. Fig. 5

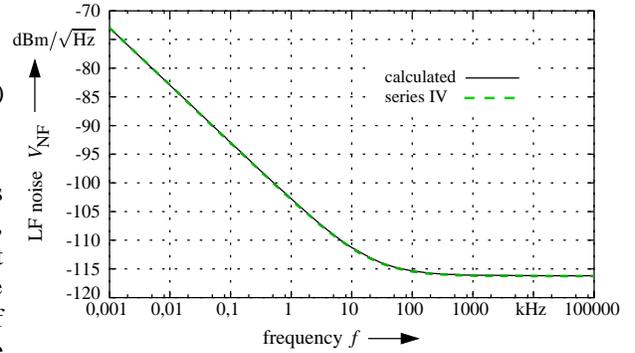


Fig. 5. Comparison of calculated $1/f$ noise for a 4-finger $50 \mu\text{m}$ HEMT at a bias point of $V_{GS} = -0.2 \text{ V}$ and $V_{DS} = 2 \text{ V}$.

shows the $1/f$ noise for a 4-finger $50 \mu\text{m}$ HEMT at a bias point of $V_{GS} = -0.2 \text{ V}$ and $V_{DS} = 2 \text{ V}$. The calcula-

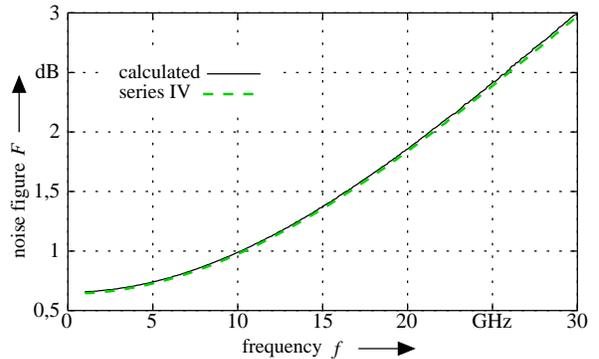


Fig. 6. Comparison of noise figure for a 4-finger $50 \mu\text{m}$ HEMT at a bias point of $V_{GS} = 0 \text{ V}$ and $V_{DS} = 2 \text{ V}$.

tion with our proposed method is compared to the one of the circuit simulation HP series IV internal model. Both calculation results are identical. In fig. 6 calculations for

the same device can be seen for the noise figure at a bias point of $V_{GS} = 0$ V and $V_{DS} = 2$ V. Again, both results are in excellent agreement. The comparison with measure-

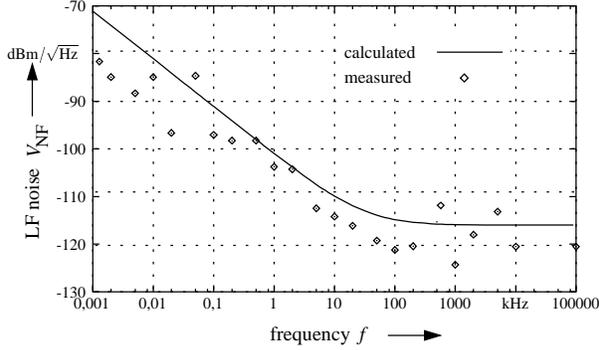


Fig. 7. Comparison of simulated and measured $1/f$ noise for a 4-finger $50 \mu\text{m}$ HEMT device at a bias point of $V_{GS} = 0$ V and $V_{DS} = 1.5$ V.

ments can be seen in the next two plots (fig. 7 and fig. 8). The same proposed algorithm was used to extract parameters for the $1/f$ noise as described in equation (16)

$$i_f^2 = k_f \frac{I^{af}}{f^b} \Delta f \quad (16)$$

for a 4-finger $50 \mu\text{m}$ HEMT device at a bias point of $V_{GS} = 0$ V and $V_{DS} = 1.5$ V. The extracted parameters are

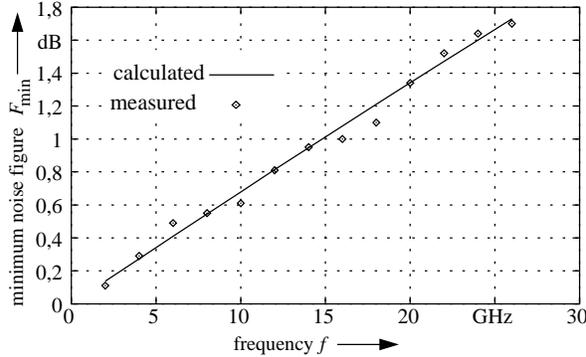


Fig. 8. Comparison of measured and simulated minimum noise figure for a 4-finger $40 \mu\text{m}$ HEMT at a bias point of $V_{GS} = -0.2$ V and $V_{GS} = 2$ V.

$k_f = 1.3 \cdot 10^{-12}$, $af = 0.9$ and $b = 1.05$. Fig. 8 shows the comparison of the simulated and measured noise figure for a 4-finger $40 \mu\text{m}$ HEMT at a bias point of $V_{GS} = -0.2$ V and $V_{GS} = 2$ V. All simulations are in excellent agreement with measurements.

VI. CONCLUSIONS

In our paper an effective method for calculating both LF as well as RF noise of active devices has been

demonstrated. The method was compared with the one used by HP-EEsof libra series IV circuit simulation software. It has been shown that both methods lead to nearly the same results. Furthermore, the method we used can easily be applied to any extraction software in order to extract e.g. coefficients for the $1/f$ noise behavior. Also, simulation results in excellent agreement to measurements have been shown.

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Rüdiger Follmann was born in Prüm, Germany, in 1968. He studied electrical engineering at the Technical University of Aachen and received the Dipl.-Ing. degree in 1995. Since 1995 he has been with the Institute of Mobile and Satellite Communication Techniques (IMST). In 1998 he received the doctoral degree from the University of

Duisburg, Germany. Since 1999 he has been head of the section "Integrated Communication Circuits" (ICC). His main interests are nonlinear device modeling and MMIC design.

Jörg Berben was born in Duisburg, Germany, in 1969. He studied electrical engineering at the FH Niederrhein and received the Dipl.-Ing. degree in 1999. Since then he has been with the Institute of Mobile and Satellite Communication Techniques (IMST). His main interests are also nonlinear device modeling and MMIC design.



Dietmar Köther was born in 1958 in Duisburg, Germany. He received the electrical engineering degree (MSc) from the University - GH - Duisburg in 1984. From 1984 to 1988 he worked at the Department of Electrical Engineering at the University - GH - Duisburg on the DFG project "Reciprocal and

nonreciprocal image guide elements utilizing premagnetized ferrits". In 1989 he got the PhD degree. From 1989 to 1992 he worked for ArguMens responsible for the development of a CAD package and measurement software. In 1991 the design study "40/80 GHz electronical MMIC circulators" started. Some activities have been done within the frame of Esprit II, Esprit III and BMBF DFE. Since 1993 he has been at the IMST where he dealt with the implementation of CPW elements into a CAD program. Furthermore, nonstandard measurement software for nonlinear characterization and active load pull has been developed. Since begin of 1995 he has been head of the section "Simulation and Measurement Software" at the IMST.

Johannes Borkes received his engineering degree (Dipl.-Eng.) in electrical engineering in 1989 from Duisburg University. From that time on he was working as a research assistant in the Department of Electrical Engineering at Duisburg University. After a short excursion in electromagnetic field theory, he worked on III-V-Semiconductor design. 1994 he joined the Institute of Mobile and Satellite Communication Techniques in Kamp-Linfort, Germany, where he worked on Microsystems techniques. Currently he holds the position as a head of department "Integrated Circuits and Systems". His particular interests are MMIC and RFIC design towards "System-On-Chip-Solutions" and associated measurement techniques as well as packaging of IC's.



Ingo Wolff (M '75 - SM '85 - F '88) was born in Köslin, Germany, in 1938. He studied electrical engineering at the Technical University of Aachen and received the Dipl.-Ing. degree in 1964. In 1967 he received the doctoral degree and in 1970 the habilitation degree, also from the Technical University of Aachen, Germany. From 1970 to 1974 he was a Lecturer and Associate Professor for

high-frequency techniques in Aachen. Since 1974 he has been a Full Professor of electromagnetic field theory at the University of Duisburg, Duisburg, Germany. His main areas of research are electromagnetic field theory applied to the computer-aided design of MIC's and MMIC's, millimeter-wave components and circuits, and the field theory of anisotropic materials.

Since 1992 he has been the director of the Institute of Mobile and Satellite Communication Techniques (IMST), Kamp-Lintfort, parallel to his university position. This institute intensively works in the area of mobile communication techniques, microwave and millimeter wave communication techniques, antenna techniques and electromagnetic compatibility. Since 1999 Prof. Wolff has been director of the University of Duisburg.